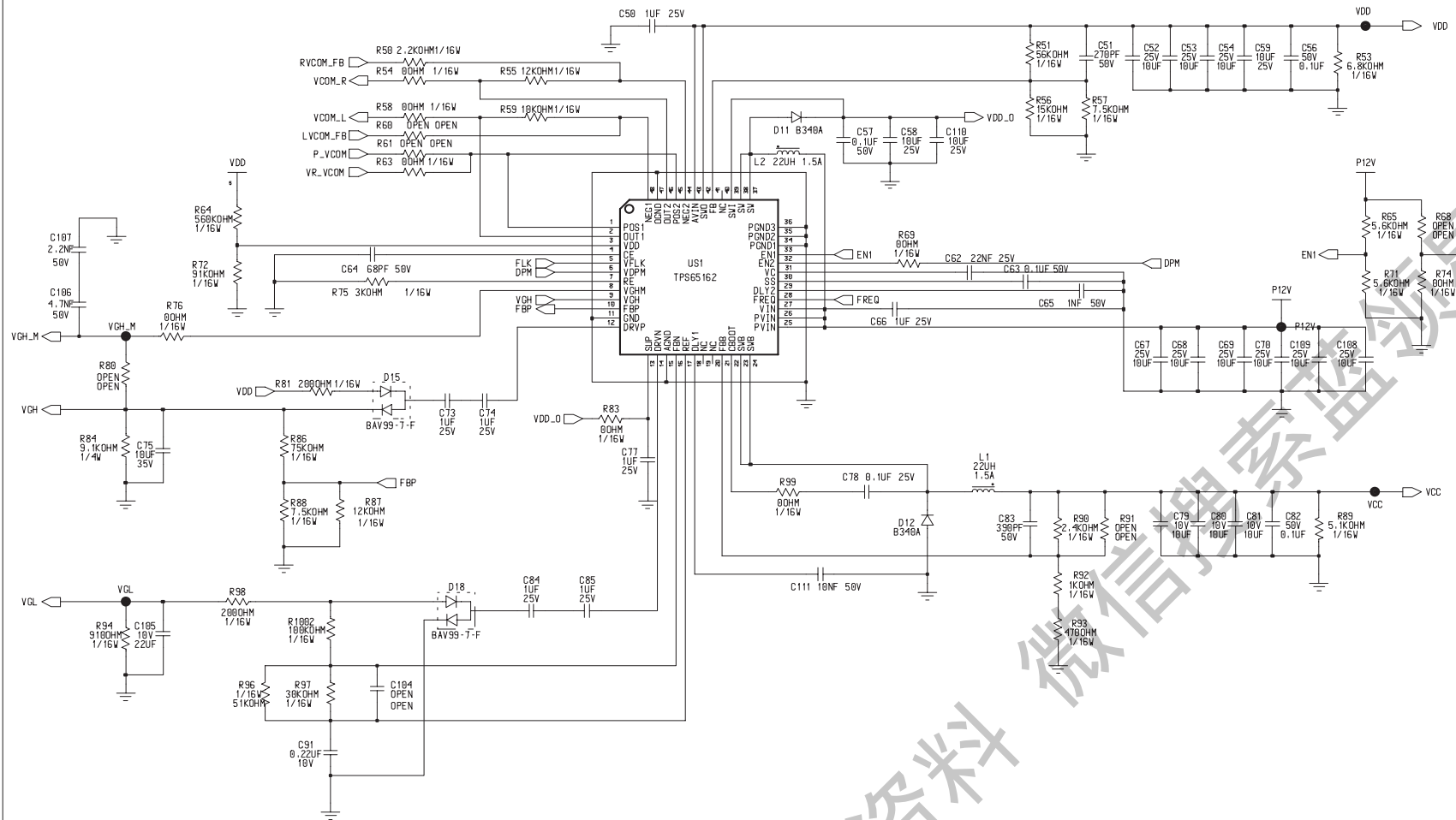
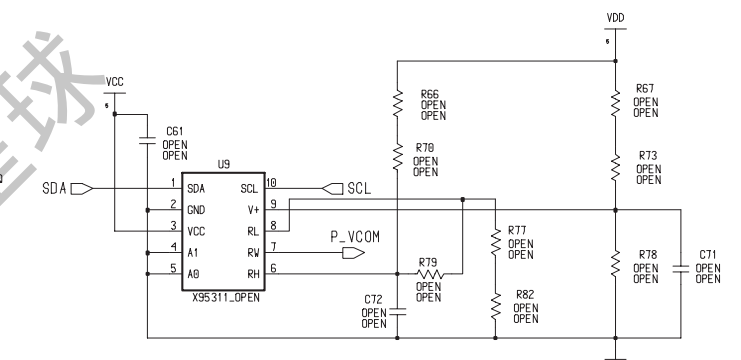


\*\* REFERENCE :  
C : -C102

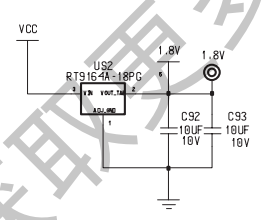
[POWER BLOCK]



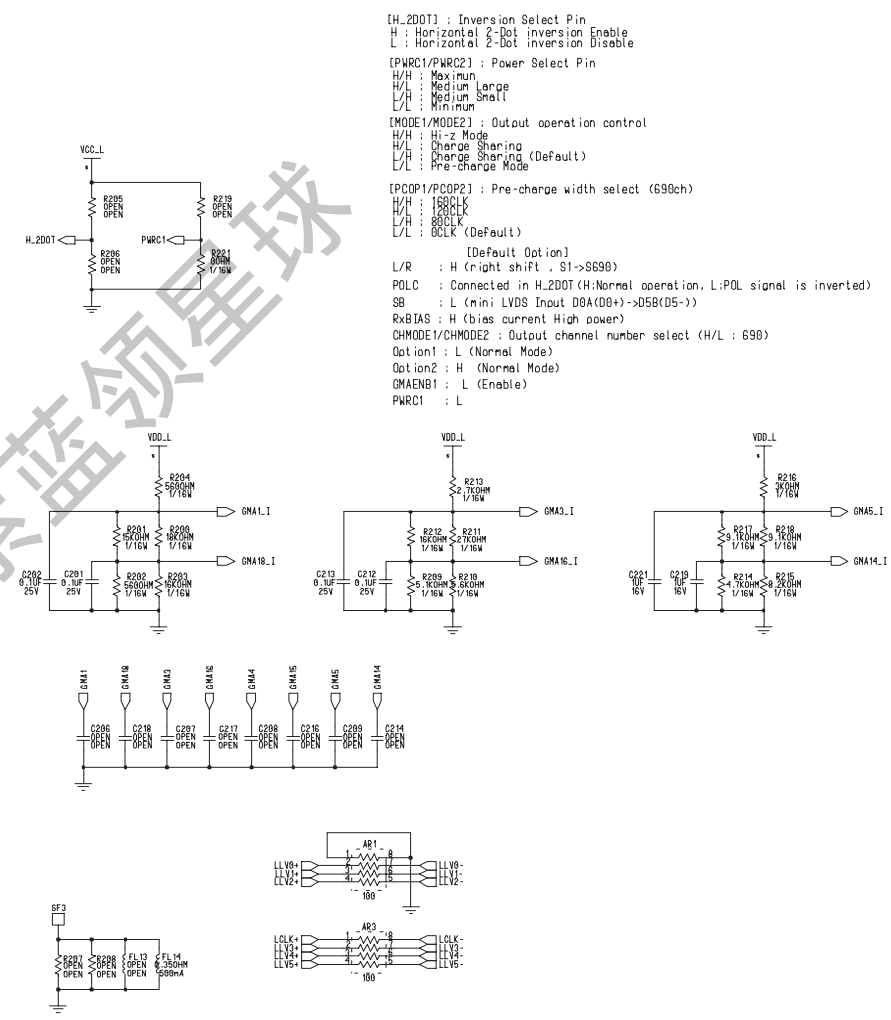
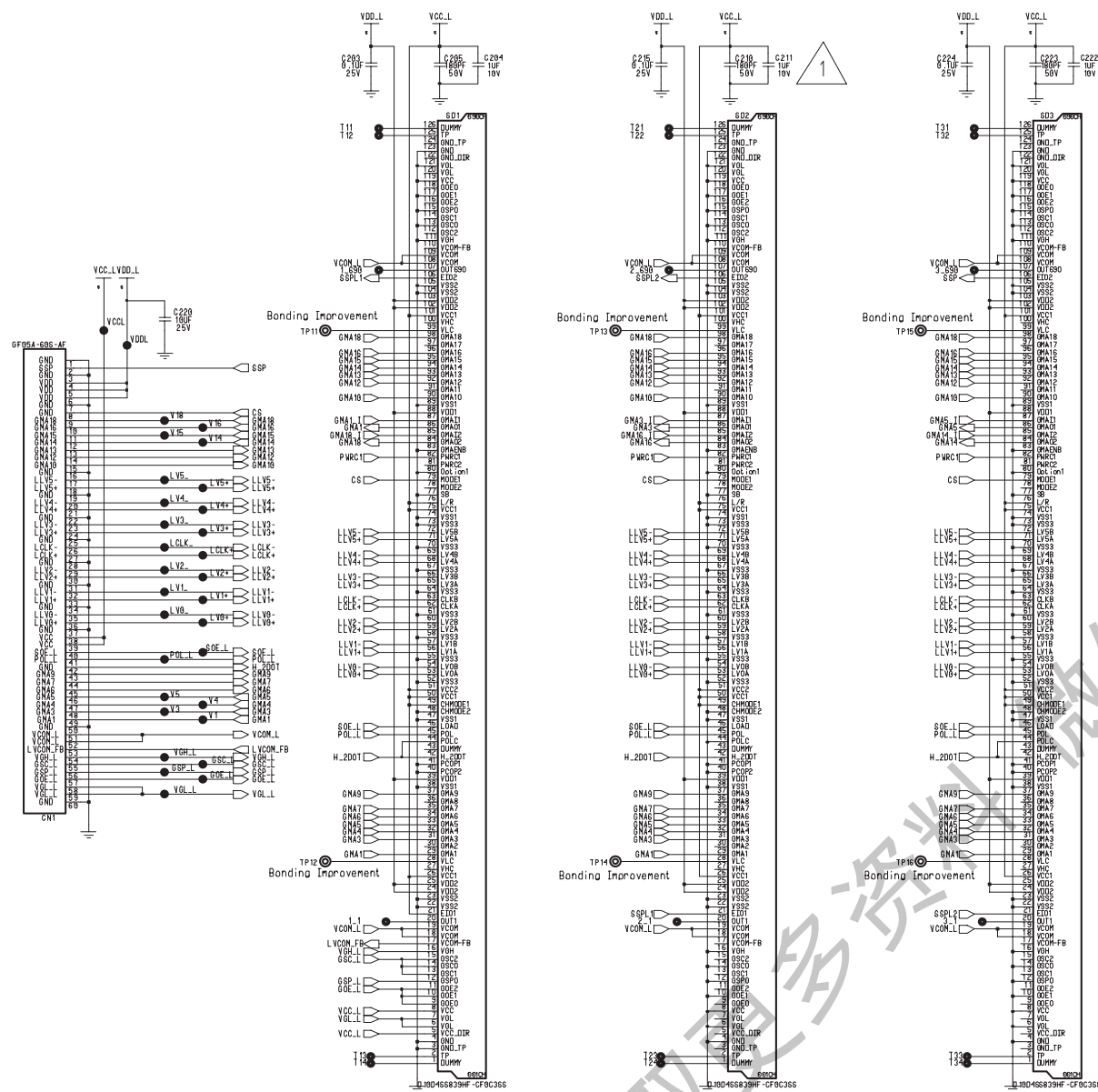
[ P-VCOM BLOCK]



[3.3V --> 1.8V BLOCK]



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[H.2D0T] : Inversion Select Pin  
 H : Horizontal 2-Dot inversion Enable  
 L : Horizontal 2-Dot inversion Disable

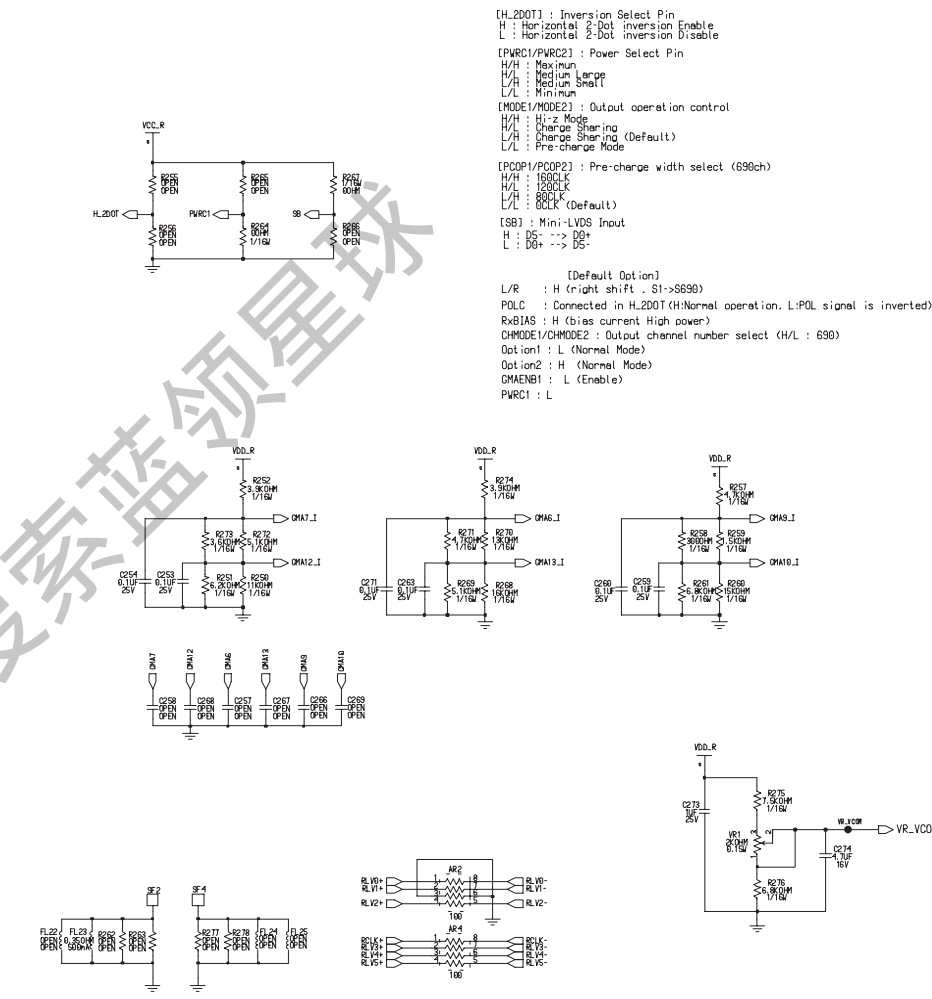
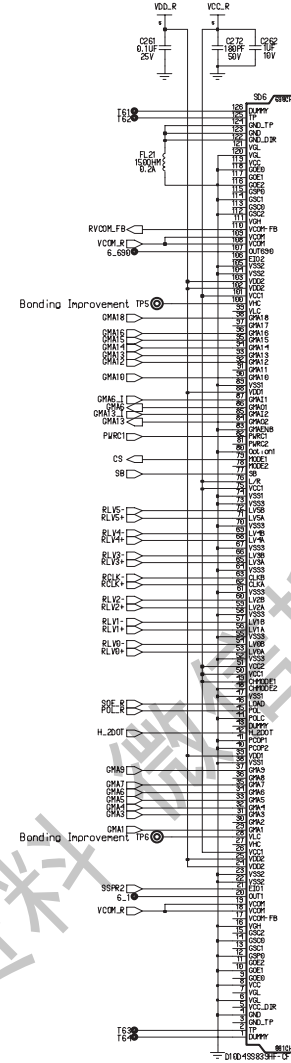
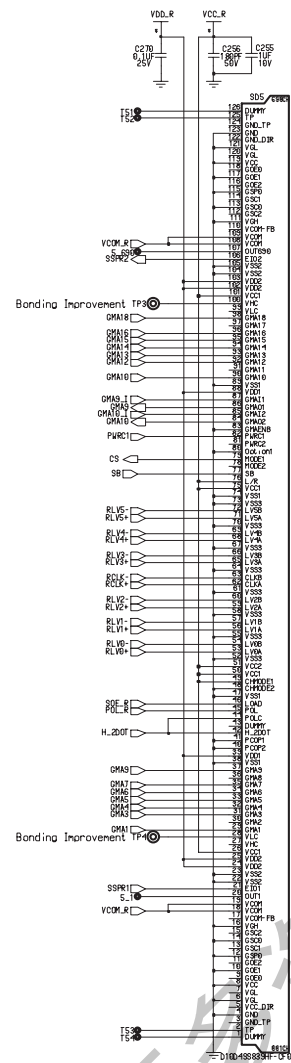
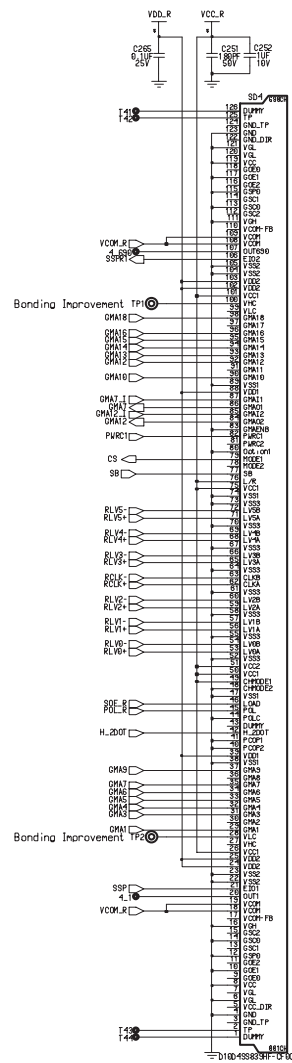
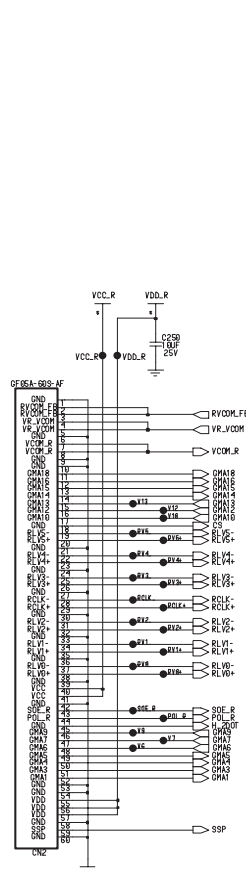
[PWR1/PWR2] : Power Select Pin  
 H/H : Maximum  
 M/L : Medium Large  
 M/S : Medium Small  
 L/L : Minimum

[MODE1/MODE2] : Output operation control  
 H/H : Hi-Z Mode  
 H/L : Charge Sharing  
 L/L : Charge Sharing (Default)  
 Pre-charge Mode

[PCOP1/PCOP2] : Pre-charge width select (690ch)  
 H/H : 160CLK  
 L/H : 80CLK  
 L/L : 0CLK (Default)

[Default Option]  
 L/R : H (right shift, S1-S690)  
 POLC : Connected in H.2D0T (H:Normal operation, L:POL signal is inverted)  
 SB : L (mini LVDS Inout DOA(D0+)-D5B(D5-))  
 RxBIAS : H (bias current High power)  
 CHMODE1/CHMODE2 : Output channel number select (H/L : 690)  
 Option1 : L (Normal Mode)  
 Option2 : H (Normal Mode)  
 GMAENB1 : L (Enable)  
 PWR1 : L

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[H.2D0T] : Inversion Select Pin  
 H : Horizontal 2-Dot inversion Enable  
 L : Horizontal 2-Dot inversion Disable

[PWR01/PWR02] : Power Select Pin  
 H/H : Maximum  
 L/H : Medium Large  
 L/L : Minimum

[MODE1/MODE2] : Output operation control  
 H/H : Hi-z Mode  
 L/H : Charge Sharing  
 H/L : Charge Sharing (Default)  
 L/L : Pre-charge Mode

[PCOP1/PCOP2] : Pre-charge width select (690ch)  
 H/H : 160CLK  
 L/H : 80CLK  
 L/L : 80CLK (Default)

[SB] : Mini-LVDS Input  
 H : D5+ -> D5-  
 L : D5+ -> D5-

[Default Option]  
 L/R : H (right shift, S1->S690)  
 POLC : Connected in H.2D0T (H:Normal operation, L:POL signal is inverted)  
 RxBIAS : H (bias current High power)  
 CHMODE1/CHMODE2 : Output channel number select (H/L : 690)  
 Option1 : L (Normal Mode)  
 Option2 : H (Enable)  
 GMAENB1 : L (Enable)  
 PWR01 : L

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